

METHOD AND APPARATUS FOR ACCELERATING TEST

CASE DEVELOPMENT

ABSTRACT OF THE DISCLOSURE

- 5 A system configured to minimize validation time associated with an integrated circuit design is provided. The system includes a client and a server. The client is configured to identify a test case for simulation with the integrated circuit design. The client is further configured to generate a verified file from the test case. The server is in communication with the client. The server is configured to maintain an initialized state.
- 10 The server, when in the initialized state, is configured to receive the verified file from the client for execution, wherein after execution of the verified file, the server is enabled to communicate results to the client and the server resets to the initialized state. A method for submitting a test case for simulation of an integrated circuit design and a computer readable medium are also provided.